

| Ref # | Hits  | Search Query  | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|-------|---|---|------------------|---------|------------------|
| L1    | 668   | 345/77  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2005/05/20 17:43 |
| S1    | 970   | (315/169.4).CCLS.   | US-PGPUB;<br>USPAT;<br>USOCR                                      | OR               | OFF     | 2005/05/18 17:32 |
| S2    | 696   | capacitive with load with driving with circuit  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2005/05/16 14:33 |
| S3    | 55    | S2 and pulse\$1 and amplify\$3  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2005/05/16 14:34 |
| S4    | 770   | S1 and @pd<"20030410"   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2005/05/16 15:56 |
| S5    | 11335 | amplify\$3 and ((pulse adj width adj modulat\$3) or (pulse with delay)) and switch\$3 | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2005/05/16 15:58 |
| S6    | 740   | S5 and ((capacitive adj load) or pixel)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2005/05/16 15:59 |
| S7    | 487   | S6 and @pd<"20030410"   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2005/05/17 09:09 |

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| S8  | 216  | (capacitive adj load) with (driving adj circuit)           | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/17 09:24 |
| S9  | 1216 | (345/60).CCLS.   | US-PGPUB;<br>USPAT;<br>USOCR                                      | OR | OFF | 2005/05/17 09:24 |
| S11 | 1216 | (345/60).CCLS.   | US-PGPUB;<br>USPAT;<br>USOCR                                      | OR | OFF | 2005/05/17 12:36 |
| S12 | 740  | S11 and @pd<"20030410"                                     | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/17 13:05 |
| S13 | 1073 | (delay same pulse) and pixel\$1 and (driv\$3 near circuit) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/17 15:58 |
| S14 | 48   | 706/35   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/17 13:11 |
| S15 | 1    | ("20020097203").PN.  | US-PGPUB;<br>USPAT;<br>USOCR                                      | OR | OFF | 2005/05/18 09:14 |
| S16 | 3479 | (pulse with modulat\$3) same width same delay              | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/18 09:16 |
| S17 | 68   | S16 and (capacitive near load)                             | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/18 09:17 |
| S18 | 651  | (capacitive adj load) and (driving adj circuit)            | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/18 17:33 |

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| S19 | 305  | S18 and pulse\$1  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/18 17:33 |
| S20 | 131  | S19 and delay\$3  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/18 17:47 |
| S21 | 1    | ("20040201552").PN.   | US-PGPUB;<br>USPAT;<br>USOCR                                      | OR | OFF | 2005/05/18 18:01 |
| S22 | 1447 | (fornt\$1edge or (rising near edge))<br>and (back\$1edge or (falling near<br>edge)) and amplify\$3 and switch | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/18 18:04 |
| S23 | 140  | S22 and (capacitive near load)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/19 09:57 |
| S24 | 1    | ("4914353").PN.   | US-PGPUB;<br>USPAT;<br>USOCR                                      | OR | OFF | 2005/05/19 09:57 |
| S25 | 1    | ("5616988").PN.   | US-PGPUB;<br>USPAT;<br>USOCR                                      | OR | OFF | 2005/05/19 09:58 |
| S26 | 1    | ("5627556").PN.   | US-PGPUB;<br>USPAT;<br>USOCR                                      | OR | OFF | 2005/05/19 09:58 |
| S27 | 1    | ("6281633").PN.   | US-PGPUB;<br>USPAT;<br>USOCR                                      | OR | OFF | 2005/05/19 09:59 |
| S28 | 1    | ("6496166").PN.   | US-PGPUB;<br>USPAT;<br>USOCR                                      | OR | OFF | 2005/05/19 10:20 |
| S29 | 2    | (pulse near modulation) with circuit<br>same ((rising and falling) adj<br>edge\$1)                            | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/19 10:26 |

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| S30 | 30   | (pulse near (modulation or delay))<br>with circuit same ((rising and<br>falling) adj edge\$1) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/19 10:55 |
| S31 | 428  | (345/212).CCLS.   | US-PGPUB;<br>USPAT;<br>USOCR                                      | OR | OFF | 2005/05/19 10:58 |
| S32 | 226  | (332/109).CCLS.   | US-PGPUB;<br>USPAT;<br>USOCR                                      | OR | OFF | 2005/05/19 10:58 |
| S34 | 218  | S32 and @pd<"20040120"  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/19 11:09 |
| S35 | 14   | S34 and pixel\$1  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/19 11:40 |
| S36 | 7799 | (low adj pass adj filter) and (pulse<br>with modulation)                                      | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/19 11:41 |
| S37 | 373  | S36 and ((rising and falling) near<br>edge\$1)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/19 12:03 |
| S38 | 1    | ("20020097203").PN.   | US-PGPUB;<br>USPAT;<br>USOCR                                      | OR | OFF | 2005/05/19 14:44 |
| S39 | 1    | ("20010055258").PN.   | US-PGPUB;<br>USPAT;<br>USOCR                                      | OR | OFF | 2005/05/19 16:24 |
| S40 | 1    | ("20020097203").PN.   | US-PGPUB;<br>USPAT;<br>USOCR                                      | OR | OFF | 2005/05/19 17:17 |
| S41 | 1    | ("20010055258").PN.   | US-PGPUB;<br>USPAT;<br>USOCR                                      | OR | OFF | 2005/05/19 17:27 |

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| S42 | 69352 | ((capacitive adj element) or capacitor) same ((resistive adj element) or resistor) same ((switch adj element) or switch)                  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/19 17:29 |
| S43 | 75    | ((capacitive adj element) or capacitor) same (((resistive adj element) or resistor) same ((switch adj element) or switch)) near parallel) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/19 17:41 |
| S44 | 19768 | (low adj pass adj filter) same (diode or switch)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/19 17:42 |
| S45 | 33    | (low adj pass adj filter) same (diode or switch) and ((resistor near parallel) near (diode or switch))                                    | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/20 10:22 |
| S46 | 1     | ("20040201552").PN.   | US-PGPUB;<br>USPAT;<br>USOCR                                      | OR | OFF | 2005/05/20 11:39 |
| S47 | 1     | pulse and front\$1edge same (falling adj edge)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/20 11:40 |
| S48 | 43273 | (input near signal with pulse)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/20 11:41 |
| S49 | 590   | ((input near signal) with pulse) same (falling near edge)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/20 11:47 |
| S50 | 1     | ("20010055258").PN.   | US-PGPUB;<br>USPAT;<br>USOCR                                      | OR | OFF | 2005/05/20 13:31 |
| S51 | 0     | ("pulsesame(modulationnearcircuit)").PN.  | US-PGPUB;<br>USPAT;<br>USOCR                                      | OR | OFF | 2005/05/20 13:31 |

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| S52 | 4547 | pulse same (modulation near circuit)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/20 13:31 |
| S54 | 894  | S52 and (((capacitive and resistive and switching) adj element) or (capacitor\$1 and resistor\$1 and switch\$2)) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/20 13:33 |
| S55 | 387  | S54 and pulse and delay  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/20 13:34 |
| S56 | 312  | S55 and @pd<"20040120"   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/20 14:12 |
| S57 | 1583 | modulation same pulse same counter same clock  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/20 14:12 |
| S58 | 247  | S57 same delay   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2005/05/20 17:43 |